CASE statement for D FF with MUX:

LIBRARY ieee ;
USE ieee.std_logic_1164.all ;

ENTITY CaseMuxFF IS
    PORT ( D0,D1,sel,clock : IN
            STD_LOGIC ;
        Q : OUT   STD_LOGIC ) ;
END CaseMuxFF ;

ARCHITECTURE Behavior OF CaseMuxFF IS
BEGIN
    PROCESS
    BEGIN
        WAIT UNTIL clock'Event AND clock = '1';
        CASE sel IS
        WHEN '0' =>  
            Q <= D0 ;
        WHEN OTHERS =>
            Q <= D1 ;
        END CASE ;
    END PROCESS ;
END Behavior ;